

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants:

H. CHAKIHARA, et al.

Application No.:

10/629,733

Filed:

July 30, 2003

For:

A SEMICONDUCTOR EMORY DEVICE AND A METHOD OF

MANUFACTURING THE SAME, A METHOD OF

MANUFACTURING A VERTICAL MISFET AND A VERTICAL

MISFET, AND A METHOD OF MANUFACTURING A SEMICONDUCTOR DEVICE AND A SEMICONDUCTOR

DEVICE

Art Unit:

2826

Examiner:

F. Abraham

RESPONSE TO ELECTION/RESTRICTION REQUIREMENT

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450 April 26, 2004

Sir:

In response to the Restriction Requirement dated March 26, 2004, Applicants hereby elect the **Group I** claims 1-23, 30-47 and 51-55, drawn to memory, classified in class 257, subclass 296, without traverse.

To the extent necessary, Applicants petition for an extension of time under 37 CFR 1.136. Please charge any shortage in fees due in connection with the filing of this paper, including extension of time fees, to the Deposit Account No. 01-2135 (Docket No. 501.42869X00), and please credit any excess fees to such Deposit Account.

Respectfully submitted,

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GEM/LNA/dlt

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